



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yasuaki HIRANO

Serial No.: 10/051,313

Filing Date: January 22, 2002

For: NONVOLATILE SEMICONDUCTOR  
MEMORY DEVICE AND METHOD OF  
ERASING THE SAME

Examiner: Hoai V. HO

Group Art Unit: 2818

Confirmation No.: 4784

**SUBMISSION OF SUBSTITUTE DRAWINGS**

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Enclosed are (18) eighteen sheets of substitute drawings in connection with the above-identified application.

Dated: March 15, 2004

Respectfully submitted,

By: Deborah S. Gladstein

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